

## **EAST Search History**

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2486	"active memory"	US-PGPUB; USPAT	OR	ON	2006/03/27 15:50
L2	0	"active memory" same DRAM same processor same embedded	US-PGPUB; USPAT	OR	ON	2006/03/27 15:50
L3	23	"active memory" and DRAM same processor same embedded	US-PGPUB; USPAT	OR	ON	2006/03/27 15:52
L4	402	DRAM with processor with embedded	US-PGPUB; USPAT	OR	ON	2006/03/27 15:52
L5	138	DRAM with processor with embedded with (chip board)	US-PGPUB; USPAT	OR	ON	2006/03/27 15:52